

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

Sheet 1 of 1

APPLICATION NO.

1572.1129

FIRST NAMED INVENTOR

Kwang-youn SEO

FILING DATE

December 11 2003

U.S. PATENT DOCUMENTS

	DOCUMENT				SUB-	FILING
	NO.	DATE	NAME	CLASS	CLASS	DATE
AA						<u> </u>
AB						
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AE						
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FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	TRANSLATION YES NO	ABSTRACT
AG					
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AK					

OTHER RE	TRANSI YES	NO		
	AM	TRIPLE SYNCHRONOUS BUCK CONTROLLER WITH NMOS LDO CONTROLLER; Texas Instruments Incorporated; 2002		
	AN	Enrico Dallago; LOSSLESS CURRENT SENSING IN LOW-VOLTAGE HIGH-CURRENT DC/DC MODULAR SUPPLIES; Member IEEE; 2000		

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not cit citation if not in conformance and not considered. Include copy of	tation is in conformance with MPEP 609. Draw line through of this form with next communication to applicant.

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Revised July 2004

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO.

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GROUP ART UNIT

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
AN	AA	6,366,068	04/02/02	Morishita			
	AB						
	AC						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CL	ASS	JB- ASS	TRANS YES	LATION NO
AN	AD	1994-25144	11/19/94	Korea		1	1		x
HIN	AE	1997-13557	03/29/97	Korea					х
AM	AF	1998-57257	09/25/98	Korea					х
NB	AG	2001-57241	07/04/01	Korea					х
AV	АН	20-203131	09/04/00	Korea					X
73 17	Al	60-198613	10/08/85	Japan					x
AP	AJ	11-220884	08/10/99	Japan					×
A B	AK	2000-14163	01/14/00	Japan					x
14 15	AL	2000-78439	03/14/00	Japan		1			х

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	DATE CONSIDERED 12/02/05
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